

**This Page is Inserted by IFW Indexing and Scanning
Operations and is not part of the Official Record**

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images include but are not limited to the items checked:

- ☐ BLACK BORDERS
- ☐ IMAGE CUT OFF AT TOP, BOTTOM OR SIDES
- ☐ FADED TEXT OR DRAWING
- ☐ BLURRED OR ILLEGIBLE TEXT OR DRAWING
- ☐ SKEWED/SLANTED IMAGES
- ☐ COLOR OR BLACK AND WHITE PHOTOGRAPHS
- ☐ GRAY SCALE DOCUMENTS
- ☐ LINES OR MARKS ON ORIGINAL DOCUMENT
- ☐ REFERENCE(S) OR EXHIBIT(S) SUBMITTED ARE POOR QUALITY
- ☐ OTHER: _____

IMAGES ARE BEST AVAILABLE COPY.

As rescanning these documents will not correct the image problems checked, please do not report these problems to the IFW Image Problem Mailbox.

Freeform Search

Database:

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Term:

L21

Display:

10

Documents in Display Format:

TI

Starting with Number

1

Generate: ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

Search

Clear

Interrupt

Search History

DATE: Thursday, September 09, 2004 [Printable Copy](#) [Create Case](#)

Set Name Query
side by side

Hit Count Set Name
result set

DB=USPT; PLUR=YES; OP=OR

<u>L22</u>	L21	38	<u>L22</u>
DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
<u>L21</u>	l19 and L20	60	<u>L21</u>
<u>L20</u>	l18 and l11	96	<u>L20</u>
<u>L19</u>	integrated or ((single or one) adj chip)	1086434	<u>L19</u>
<u>L18</u>	l13 and l4 and l6	752	<u>L18</u>
<u>L17</u>	l11 and L16	17	<u>L17</u>
<u>L16</u>	l15 and l4 and l6 and l8 and l13	92	<u>L16</u>
<u>L15</u>	l1 near4 l2	7576	<u>L15</u>
<u>L14</u>	l13 and l12	20	<u>L14</u>
<u>L13</u>	(l1 or l2) adj2 controller	18589	<u>L13</u>
<u>L12</u>	l9 and L11	41	<u>L12</u>
<u>L11</u>	711/\$.ccls.	22122	<u>L11</u>
<u>L10</u>	711/\$.ccls.L9	22358	<u>L10</u>
<u>L9</u>	l7 and L8	277	<u>L9</u>
<u>L8</u>	error near2 (detect\$ or correct\$)	211146	<u>L8</u>
<u>L7</u>	l3 and l4 and L6	632	<u>L7</u>
<u>L6</u>	decompression	34904	<u>L6</u>

<u>L5</u>	decompression	L4	0	<u>L5</u>
<u>L4</u>	compression		685914	<u>L4</u>
<u>L3</u>	l1 with L2		12053	<u>L3</u>
<u>L2</u>	register or channel		1850796	<u>L2</u>
<u>L1</u>	cache		74437	<u>L1</u>

END OF SEARCH HISTORY

Collections

Definition, Editing, Browsing

Name: Undefined

Contents:

6515759
6393545
6145069
6237083
5875454

Comment:

Database:

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Save

Save As

Reset

Quit

Print

Search

Get Images

Classification Info

Collection Directory

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

IEEE Enterprise

- ☐ Access the IEEE Enterprise File Cabinet

 Print Format
Your search matched **147** of **1071730** documents.A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance** in **Descending** order.

Refine This Search:

You may refine your search by editing the current search expression or entering a new one in the text box.

compression <and> decompression <and> (integrated

Search

☐ Check to search within this result set

Results Key:

JNL = Journal or Magazine **CNF** = Conference **STD** = Standard**1 Test data compression and decompression based on internal scan chains and Golomb coding***Chandra, A.; Chakrabarty, K.;*Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on , Volume: 21 , Issue: 6 , June 2002
Pages:715 - 722[\[Abstract\]](#) [\[PDF Full-Text \(421 KB\)\]](#) IEEE JNL**2 On the design and implementation of a lossless data compression and decompression chip***Royals, D.M.; Markas, T.; Kanopoulos, N.; Reif, J.H.; Storer, J.A.;*Solid-State Circuits, IEEE Journal of , Volume: 28 , Issue: 9 , Sept. 1993
Pages:948 - 953[\[Abstract\]](#) [\[PDF Full-Text \(604 KB\)\]](#) IEEE JNL**3 Test data compression for system-on-a-chip using Golomb codes***Chandra, A.; Chakrabarty, K.;*VLSI Test Symposium, 2000. Proceedings. 18th IEEE , 30 April-4 May 2000
Pages:113 - 120[\[Abstract\]](#) [\[PDF Full-Text \(124 KB\)\]](#) IEEE CNF**4 Deterministic test vector decompression in software using linear operations [SOC testing]***Balakrishnan, K.J.; Toubia, N.A.;*VLSI Test Symposium, 2003. Proceedings. 21st , 27 April-1 May 2003
Pages:225 - 231[\[Abstract\]](#) [\[PDF Full-Text \(787 KB\)\]](#) IEEE CNF**5 System-on-a-chip test-data compression and decompression**

architectures based on Golomb codes

Chandra, A.; Chakrabarty, K.;

Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on , Volume: 20 , Issue: 3 , March 2001

Pages:355 - 368

[\[Abstract\]](#) [\[PDF Full-Text \(380 KB\)\]](#) IEEE JNL

6 MARVLE: a VLSI chip for data compression using tree-based codes

Mukherjee, A.; Ranganathan, N.; Flieder, J.; Acharya, T.;

Very Large Scale Integration (VLSI) Systems, IEEE Transactions on , Volume: 1 , Issue: 2 , June 1993

Pages:203 - 214

[\[Abstract\]](#) [\[PDF Full-Text \(1204 KB\)\]](#) IEEE JNL

7 A single chip decompression LSI based on ATRAC for mini disc

Fuma, M.; Okamoto, M.; Kawahara, K.; Nagao, F.; Matsui, M.;

Consumer Electronics, IEEE Transactions on , Volume: 39 , Issue: 3 , Aug. 1993

Pages:356 - 363

[\[Abstract\]](#) [\[PDF Full-Text \(580 KB\)\]](#) IEEE JNL

8 Integrated test data decompression and core wrapper design for low-cost system-on-a-chip testing

Gonciari, P.T.; Al-Hashimi, B.M.; Nicolici, N.;

Test Conference, 2002. Proceedings. International , 7-10 Oct. 2002

Pages:64 - 73

[\[Abstract\]](#) [\[PDF Full-Text \(797 KB\)\]](#) IEEE CNF

9 1-cycle code decompression circuitry for performance increase of Xtensa-1040-based embedded systems

Lekatsas, H.; Henkel, J.; Jakkula, V.;

Custom Integrated Circuits Conference, 2002. Proceedings of the IEEE 2002 , 12-15 May 2002

Pages:9 - 12

[\[Abstract\]](#) [\[PDF Full-Text \(542 KB\)\]](#) IEEE CNF

10 An efficient test vector compression scheme using selective Huffman coding

Jas, A.; Ghosh-Dastidar, J.; Mom-Eng Ng; Touba, N.A.;

Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on , Volume: 22 , Issue: 6 , June 2003

Pages:797 - 806

[\[Abstract\]](#) [\[PDF Full-Text \(527 KB\)\]](#) IEEE JNL

11 Portable video-on-demand in wireless communication

Meng, T.H.; Gordon, B.M.; Tsern, E.K.; Hung, A.C.;

Proceedings of the IEEE , Volume: 83 , Issue: 4 , April 1995

Pages:659 - 680

[\[Abstract\]](#) [\[PDF Full-Text \(1928 KB\)\]](#) IEEE JNL

12 A single chip compression/decompression LSI based on JPEG

Ogawa, K.; Urano, T.; Kondo, K.; Mori, N.; Moriai, S.; Yamamoto, H.; Kato, S.;
Consumer Electronics, IEEE Transactions on , Volume: 38 , Issue: 3 , Aug 1992
Pages:703 - 710

[\[Abstract\]](#) [\[PDF Full-Text \(656 KB\)\]](#) **IEEE JNL**

13 Test vector compression via statistical coding and dynamic compaction

Mom Eng Ng; Touba, N.A.;
AUTOTESTCON Proceedings, 2000 IEEE , 18-21 Sept. 2000
Pages:348 - 354

[\[Abstract\]](#) [\[PDF Full-Text \(448 KB\)\]](#) **IEEE CNF**

14 X-MatchPRO: a high performance full-duplex lossless data compressor on a ProASIC FPGA

Nunez, J.L.; Jones, S.; Bateman, S.;
Intelligent Data Acquisition and Advanced Computing Systems: Technology and Applications, International Workshop on, 2001. , 1-4 July 2001
Pages:56 - 60

[\[Abstract\]](#) [\[PDF Full-Text \(456 KB\)\]](#) **IEEE CNF**

15 Voice compression of low complicated decomposition

Huang Defeng; Zhou Zucheng;
Communication Technology Proceedings, 1998. ICCT '98. 1998 International Conference on , Volume: vol.2 , 22-24 Oct. 1998
Pages:5 pp. vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(328 KB\)\]](#) **IEEE CNF**

[1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [Next](#)
